

(19)



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11) Publication number: **2002232322 A**

(43) Date of publication of application: **16.08.02**

(51) Int. Cl

H04B 1/707

(21) Application number: **2001026603**

(22) Date of filing: **02.02.01**

(71) Applicant: **FUJITSU LTD**

(72) Inventor: **KAWAGUCHI NORIYUKI
MINOWA MORIHIKO
KIMURA MASARU
TAKEUCHI MASAJI**

(54) MATCHING FILTER AND SEARCHER

(57) Abstract:

PROBLEM TO BE SOLVED: To reduce a circuit scale and power consumption of a searcher and a matched filter.

SOLUTION: This matched filter is provided with two shift registers 531 and 532 holding a chip number smaller than the chip number of received preamble signals. The preamble signals from a first antenna by diversity reception are held in the shift register 531, and the preamble signals from a second antenna are held in the shift register 532, respectively. An SEL 533 selects one of the shift registers 531 and 532 and supplies chip data held in the selected shift register to a multiplier 534. In the multiplier 534, the chip data and an inverse spread code are multiplied, and multiplied results are supplied to an addition part 535. In the addition part 535, the multiplied results are added, and an added result is supplied to the pattern cancellation/common-mode addition part of the poststage.

COPYRIGHT: (C)2002,JPO

